

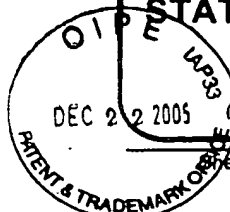
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**SUPPLEMENTAL INFORMATION  
DISCLOSURE  
STATEMENT BY APPLICANT**

Application Number	10/666,948
Filing Date	September 19, 2003
First Named Inventor	Stephen J. Smith
Art Unit	2115
Examiner Name	Suresh Suryawanshi
Attorney Docket Number	174/161 Cont.
Confirmation Number	7049

(Use as many sheets as necessary)

Sheet 1 of 2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
SKS		5,541,849	07-30-1996	Rostoker et al.	
SKS		5,819,064	10-06-1998	Razdan et al.	
SKS		6,044,211	03-28-2000	Jain	
SKS		6,226,776	05-01-2001	Panchul et al.	
SKS		6,457,173	09-24-2002	Gupta et al.	
SKS		6,477,683	11-05-2002	Killian et al.	
SKS		6,608,638	08-19-2003	Kodosky et al.	
SKS		6,625,797	09-23-2003	Edwards et al.	
SKS		6,745,160	06-01-2004	Ashar et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
SKS		WO 97/09930	03-20-1997	Medison Co., Ltd.		

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	
SKS		IBM, "Programmable Manual Cable Assembly Board," IBM Technical Disclosure Bulletin, Vol. 31, No. 12, May 1989, pp. 306-309.
SKS		Nanya, "Asynchronous VSLI System Design," ASP-DAC '98 Tutorials, February 10, 1998, Yokohama, Japan.

Examiner Signature Suresh K SuryawanshiDate Considered 3/1/06

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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Stg

Nanya et al. "Scalable-Delay-Insensitive Design: A high-performance approach to dependable asynchronous systems," Proceedings of International Symposium on Future of Intellectual Integrated Electronics, March, 1999, pp. 1-10.

Examiner  
Signature

Suresh K. Suryawanshi

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7/1/06

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